GP/2123 #3

TRANSMITTAL OF FORMAL DRAWINGS

Docket No.
200-0664

In Re Application Of: **J. G. Walacavage et al.**

Serial No.	Filing Date	Batch No.	Examiner	Art Unit
09/965,905	September 28, 2001			2123

Invention: **METHOD OF PART FLOW MODEL FOR PROGRAMMABLE LOGIC CONTROLLER LOGICAL VERIFICATION SYSTEM**

Address to:
Assistant Commissioner for Patents
Washington, D.C. 20231

Transmitted herewith are:

1 &2 copies sheets of formal drawing(s) for this application.

Each sheet of drawing indicates the identifying indicia suggested in 37 CFR Section 1.84(c) on the reverse side of the drawing.

Signature

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Dated: **November 1, 2001**

I certify that this document and attached formal drawings are being deposited on Nov. 1 2001 with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

Signature of Person Mailing Correspondence

Daniel H. Bliss

Typed or Printed Name of Person Mailing Correspondence